



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Patrizio Vinciarelli
Serial No. : 10/802,379
Filed : March 17, 2004
Title : FACTORIZED POWER ARCHITECTURE WITH POINT OF LOAD SINE
AMPLITUDE CONVERTERS

Art Unit : 2838
Examiner : Jeffrey L. Sterrett

MAIL STOP AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Applicants request consideration of the references listed on the attached PTO-1449 form. Under 37 C.F.R. § 1.98 (a)(2)(ii), only copies of foreign patent documents and/or non-patent literature are enclosed. Copies of any listed U.S. patents or U.S. patent application publications can be provided upon request. Copies of three European Search Reports from related cases are also enclosed.

This statement is being filed after a first Office action on the merits, but before receipt of a final Office action or a Notice of Allowance. A check for \$180 in payment of the late submission fee of §1.17(p) is enclosed. Please apply any other charges or credits to Deposit Account No. 06-1050, referencing Attorney Docket No. 00614-136002.

02/16/2005 EAREGAY1 00000004 10802379

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CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

February 10, 2005

Date of Deposit

Signature

Carol Tyrrell

Carol Tyrrell

Typed or Printed Name of Person Signing Certificate

Applicant : Patrizio Vinciarelli
Serial No. : 10/802,379
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Page : 2 of 2

Attorney's Docket No.: 00614-136002

Respectfully submitted,

Date: Feb. 10, 2005



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Substitute Form PTO-1449 (Modified)	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 00614-136002	Application No. 10/802,379
Information Disclosure Statement by Applicant (Use several sheets if necessary) (37 CFR §1.98(b))		Applicant Patrizio Vinciarelli	
		Filing Date March 17, 2004	Group Art Unit 2838

U.S. Patent Documents

Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
	AA	5,073,848	12/17/91	Steigerwald et al.			
	AB	5,500,791	03/19/96	Kheraluwala et al.			
	AC	5,576,940	11/19/96	Steigerwald et al.			
	AD	5,726,849	03/10/98	Nakamura			
	AE	5,861,738	01/19/99	Becker-Irvin et al.			
	AF	6,069,811	05/30/00	Moriguchi et al.			
	AG	6,310,792	10/30/01	Drobnik			
	AH	6,583,999	06/24/03	Spindler et al.			

Foreign Patent Documents or Published Foreign Patent Applications

Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation	
							Yes	No
	AI	EP 0550167	07/07/93	EP				
	AJ	EP 0696831	02/14/96	EP				
	AK	DE 19546936	06/19/97	Germany				
	AL	WO 98/11658	03/19/98	WIPO				
	AM	EP 0848485	06/17/98	EP				
	AN	DE 19837639	03/02/00	Germany				

Other Documents (include Author, Title, Date, and Place of Publication)

Examiner Initial	Desig. ID	Document
	AO	Brakus, B., "DC/DC Modules for Low Voltage Applications. The New Generation of Board Mounted Modules in Thick-Copper Multi-Layer Technology", IEEE, 1998, pp. 392-397.
	AP	Casey et al., "A High-Frequency, Low Volume, Point-of-Load Power Supply for Distributed Power Systems," IEEE Transactions on Power Electronics, Vol. 3, No. 1, January 1988.
	AQ	Hua et al., "Development of a DC Distributed Power System", IEEE, 1994, pp. 763-769.

Examiner Signature	Date Considered
EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	